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## EDUCATION

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**University of Washington, Dept. of Computer Science & Engineering**  
*Seattle, Washington*

- Ph.D., Computer Science, 2006
- Thesis title: Visual Pattern Classification in Analog VLSI
- Advisors: Chris Diorio (chair), Rajesh P.N. Rao, Carl Ebeling, Pedro Domingos
- M.S., Computer Science, 2002

**University of Pittsburgh**  
*Pittsburgh, Pennsylvania*

- B.S., Computer Engineering, 2000

**ITESM, Tecnológico de Monterrey**  
*Monterrey, Nuevo León, México*

- Language and engineering study in 1997

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## EXPERIENCE

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**Principal Engineer** 2006-Present  
*V2Green, Inc.*

- Responsible for product design and development, specifically embedded systems software and hardware
- Member of management team, responsible for company direction

**Software / Embedded Systems Engineer** 2002-2004  
*Impinj, Inc.*

- Developed prototype RFID reader (software and hardware) for lab usage.
- Performed RFID system-modeling to support protocol and specification development.
- Maintained CAD environment infrastructure.

**Instructor – Introduction to VLSI (CSE468)** 2006  
*University of Washington, Dept. of Computer Science & Engineering*

- Taught a mixed graduate/undergraduate course in Digital VLSI systems, including lectures, exams, evaluation, and student project management.

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## HONORS AND AWARDS

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- Intel Ph.D. Fellow, University of Washington 2004-2005
- ARCS Fellow, University of Washington 2000-2002
- Intel M.S. Fellow, University of Washington 2000-2002
- IEC Everitt Award, University of Pittsburgh 1999
- Tau Beta Pi, University of Pittsburgh 1999